



Attorney's Pocket No.: 2102397-991260

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Hieu Van Tran et al.

Serial No.: 10/044,821

Group Art Unit: 2818

Filed: January 10, 2002

Examiner: Not Yet Assigned

Title: **BIAS DISTRIBUTION NETWORK FOR DIGITAL MULTILEVEL  
NONVOLATILE FLASH MEMORY**

#1/2/AA  
Bell  
9/2/02

Certificate of Mailing

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as FIRST CLASS MAIL in an envelope addressed to: BOX MISSING PARTS, Commissioner of Patents, Washington, D.C. 20231, on:

April 5, 2002

*Rosa A. Caviedes*

Rosa A. Caviedes

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PRELIMINARY AMENDMENT

Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

Before examination of this application, please amend the drawings as follows:

IN THE DRAWINGS:

In Figure 2:

The Ispbias box has been labeled 202.

The IBNSP<0> connected to the box 208 on the right side of the page has been changed to IBNSP<4>.

In Figure 3:

The inverter 378 was corrected to be "inverter 375".

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